	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	2018	program adj counter WITH register SAME processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 08:35	
2	BRS	L2	1279	(program adj counter WITH register) WITH processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 08:35	
3	BRS	L3	1279	(program adj counter WITH register) WITH processors	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 08:37	
4	BRS	L4	7	3 and I/O adj link	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 09:17	
5	BRS	L5	283	1 and ("64" NEAR3 register$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 09:19	